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HIGH BANDWIDTH MEMORY (HBM) DRAM: JESD235: Oct 2013. The HBM DRAM is tightly coupled to the host compute die with a distributed interface.

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High Bandwidth Memory (HBM) is a high-performance RAM interface for 3D-stacked The competing technology is the incompatible Hybrid Memory Cube interface from

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Patents Publication number High bandwidth memory interface: US8661285: Jun 29, 2011: Feb 25, 2014: Uniquify, Incorporated: Dynamically calibrated DDR memory

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IEEE Standard for High-Bandwidth Memory Interface Based on Scalable Coherent RamLink is an applicable interface for other RAM-like devices as

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### **Patent us20150078057 - high bandwidth memory**

Several generations of high bandwidth DRAM-type memory devices an improved high bandwidth chip-to-chip interface for Service - About Google Patents

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High Impact Data Visualization with Power View, Emerging Memory Technologies Yuan Xie Engineering Asset Management 2011 Jay Lee, Jun Ni,

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interface and packaging standards for many generations from the very beginning of semiconductor memory including DRAM, HIGH BANDWIDTH MEMORY (HBM) DRAM: JESD235

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